

From Program Verification to Certified Binaries

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What is this about?

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OVERALL RATING: -4 (unacceptable for presentation)

REVIEWER'S CONFIDENCE: 3 (high)

----- REVIEW -----

This short paper replays the decade old vision of proof-carrying code, but aiming to increase the level of ambition from simple memory and control-flow properties to arbitrary program properties.

(snip)

I was unable to spot any research contributions or novelty in the paper.

(snip)

In summary, this work is much too preliminary and is in the current state unacceptable for presentation.

So, what is this all about?

- ▶ A position paper, not much of a research paper
- ▶ Goal? the construction of certified software
i.e. that provably satisfies its specifications
- ▶ Why? the Holy Grail of software engineering!
- ▶ How? by combining formal verification
and proof-carrying code

Outline

Introduction

- Program verification
- Proof-carrying code

Motivation

A Hybrid System

A Motivating Example

Proof-preserving Compilation

Conclusion

Introduction (i)

- ▶ Program verification
 - ▶ aims at formally proving program correctness
 - ▶ given a formal specification or property
 - ▶ long tradition (4 decades)
 - ▶ several formal logics (e.g. Hoare Logic)
 - ▶ at the source code level

Introduction (ii)

- ▶ Proof-carrying code (PCC)
 - ▶ certified binary: a value together with a proof that the value satisfies a given specification
 - ▶ relatively recent approach (~10 years)
 - ▶ essential in modern distributed computer systems
 - ▶ executable code is transferred among devices that do not necessarily trust one another
 - ▶ at a lower level (e.g. machine language)
 - ▶ mainly interested in relatively simple properties: memory safety and control flow

Introduction (iii)

- ▶ Type-theoretic approaches to PCC
 - e.g. Shao *et al.*, POPL 2002, TOPLAS 2005;
Crary and Vanderwaart, ICFP 2002
 - ▶ arbitrary program properties
 - ▶ embedding of logic "formulae as types"
 - ▶ proof-preserving compilation
 - ▶ makes the proof a part of the code
 - ▶ type (proof) inference is undecidable

Motivation

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programmer “friendly”		
high-level proofs		
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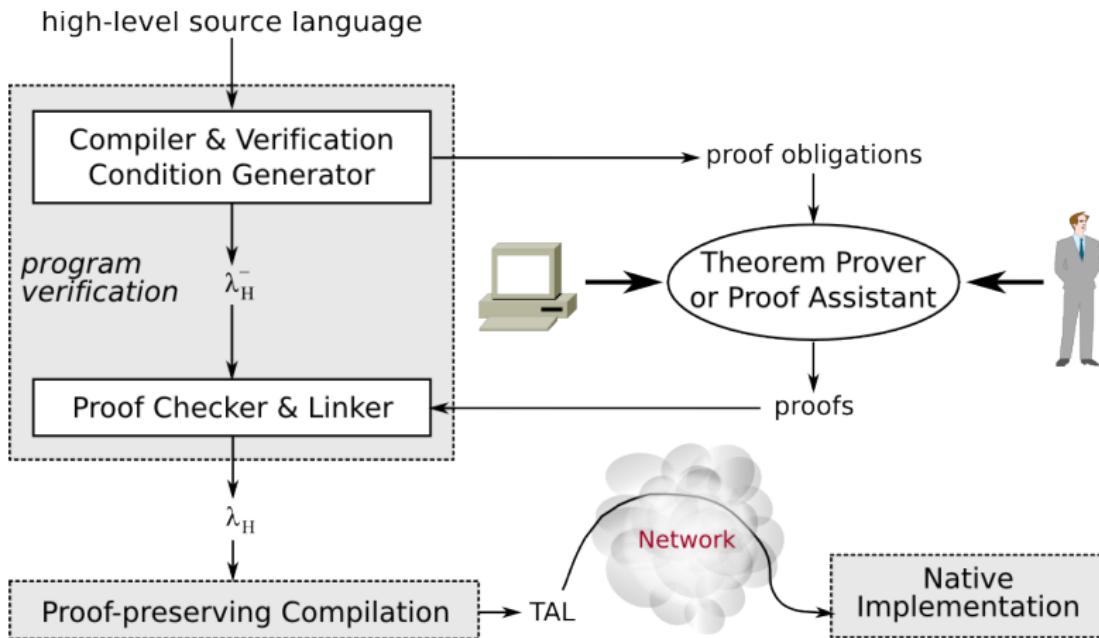
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Can we write programs in a **high-level** language,
provide **correctness proofs** for them
and then **compile** them
to **provably correct executable** code?

A hybrid system



Integer square root (i)

- Given $n \in \mathbb{N}$, find the **greatest** $r \in \mathbb{N}$ such that $r^2 \leq n$

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int root (int n) {  
    int y = 0;  
  
    while ((y+1)*(y+1) <= n) y++;  
    return y;  
}
```

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- Given $n \in \mathbb{N}$, find the greatest $r \in \mathbb{N}$ such that $r^2 \leq n$

```
//@ predicate leRoot(int r, int x) { r ≥ 0 ∧ r² ≤ x }
//@ predicate isRoot(int r, int x) { leRoot(r, x) ∧ (r + 1)² > x }
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int root (int n) {
    int y = 0;
    //@ invariant leRoot(y, n)
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}
```

Integer square root (ii)

Proof obligations in Why/Caduceus

1. $\forall n \in \mathbb{Z}.$

$$n \geq 0 \Rightarrow \text{leRoot}(0, n)$$

2. $\forall n, y \in \mathbb{Z}.$

$$n \geq 0 \wedge \text{leRoot}(y, n) \wedge (y + 1)^2 \leq n \Rightarrow \text{leRoot}(y + 1, n)$$

3. $\forall n, y \in \mathbb{Z}.$

$$n \geq 0 \wedge \text{leRoot}(y, n) \wedge (y + 1)^2 > n \Rightarrow \text{isRoot}(y, n)$$

They are all automatically discharged, using the definitions of **leRoot** and **isRoot**

Integer square root (ii)

Proof obligations

translation to \overline{H}

```
root  ▷  ∀n:Z. ∀n*: (n ≥ 0). sint n → ∃x:Z. ∃x*: isRoot x n. sint x
      = poly n:Z. poly n*: (n ≥ 0). lambda n: sint n.
        (fix loop: ∀y:Z. ∀y*: leRoot y n. sint y →
         ∃x:Z. ∃x*: isRoot x n. sint x.
          poly y:Z. poly y*: leRoot y n. lambda y: sint y.
          if [♣, ♣] ((y + cint[1])² > n,
           p₁*. pack(y, pack(♣, y) as ∃y*: isRoot y n. sint y) as
           ∃x:Z. ∃x*: isRoot x n. sint x,
           p₂*. loop [y + 1] [♣] (y + cint[1]))
          [0] [♣] cint [0]
```

Integer square root (iii)

Proof obligations from \overline{H} to H

root $\triangleright \forall n: \mathbb{Z}. \forall n^*: (n \geq 0). \text{sint } n \rightarrow \exists x: \mathbb{Z}. \exists x^*: \text{isRoot } x n. \text{sint } x$
= poly $n: \mathbb{Z}. \text{poly } n^*: (n \geq 0). \lambda n: \text{sint } n.$
(fix loop: $\forall y: \mathbb{Z}. \forall y^*: \text{leRoot } y n. \text{sint } y \rightarrow \exists x: \mathbb{Z}. \exists x^*: \text{isRoot } x n. \text{sint } x.$
poly $y: \mathbb{Z}. \text{poly } y^*: \text{leRoot } y n. \lambda y: \text{sint } y.$
if [decidable $((y + 1)^2 > n)$, gtDecidablePrf $(y + 1)^2 n$] (
 $(y + \text{cint}[1])^2 > n,$
 $p_1^*. \text{pack}(y, \text{pack}(\text{conj } y^* p_1^*, y) \text{ as } \exists y^*: \text{isRoot } y n. \text{sint } y) \text{ as } \exists x: \mathbb{Z}. \exists x^*: \text{isRoot } x n. \text{sint } x,$
 $p_2^*. \text{loop } [y + 1] [\text{conj} (\text{Zplus_ge_compat } y 0 1 0
(\text{proj1 } y^*) (\text{geDecidablePrf } 1 0))
(\text{Znot_gt_le } (y + 1)^2 n p_2^*)]$
 $(y + \text{cint}[1])))$
[0] [$\text{conj} (\text{Z_ge_refl } 0) (\text{Zge_le } n 0 n^*)$] cint [0]

Proof-preserving compilation (i)

Continuation passing style (CPS) from H to K

- ▶ Functions do not return
- ▶ One more parameter: the continuation
- ▶ Jumps instead of calls
- ▶ Control flow is explicit
- ▶ Optimizing transformations can be applied
- ▶ ~20 lines for the square root example

Proof-preserving compilation (ii)

Closure conversion

from λ_K to λ_C

- ▶ Functions only use **local data**
- ▶ One more parameter: the **closure**
- ▶ More **optimizing** transformations can be applied
- ▶ **~200 lines** for the square root example

Proof-preserving compilation (iii)

Hoisting

from C to A

- ▶ All functions become **top-level** blocks
- ▶ Memory allocation is **explicit**
- ▶ ~ 200 lines for the square root example

Proof-preserving compilation (iv)

Typed assembly language (TAL) from A to TAL

- ▶ RISC
- ▶ We assume infinite registers
 - ▶ no spilling phase
 - ▶ trivial register allocation
- ▶ We assume a garbage collector
- ▶ ~500 lines for the square root example

Proof-preserving compilation (v)

Beyond compilation

- ▶ Low Level Virtual Machine (LLVM)
- ▶ Direct translation from TAL to LLVM
- ▶ Direct translations from LLVM to native code for many architectures

*x86, x86-64, PowerPC 32/64, ARM, Thumb,
IA-64, Alpha, SPARC, MIPS, CellSPU*

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Thank you!

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